

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,953,983 B2  
DATED : October 11, 2005  
INVENTOR(S) : Paul A. Farrar

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [54], Title, insert -- **A SEMICONDUCTOR DEVICE COMPRISING** -- before "LOW".

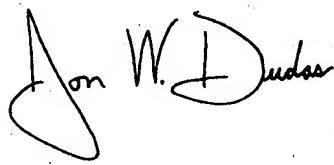
Item [56], **References Cited, OTHER PUBLICATIONS**, delete "400-406" and insert -- 400-408. --.

Column 6.

Line 58, delete "40 $\mu$ n/m $^{\circ}$  C." and insert -- 40 $\mu$ m/m $^{\circ}$  C. --.

Signed and Sealed this

Twenty-eighth Day of March, 2006



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*